



For immediate release:

For more information please contact:
Emmanuel VAUMORIN
Tel: 01 40 21 35 50
press@magillem.com

Magillem Design Services at DAC 2008: Empowering Collaborative Design and Parallel Track Development

Anaheim, June 9, 2008 - Magillem Design Services will demonstrate at DAC 2008 (Booth 2229) its new fully featured flow optimization suite comprised of an IP-XACT packager, a platform assembly tool, a complete development environment, a flow control tool and a register view kit.

The Magillem suite is a one-of-a-kind innovative software tree, enabling flawless and homogeneous design flow integration for various targets such as ASICs, FPGAs, electronic boards, AMS systems, complex systems. Adaptation kits have been developed for each target.

Add-ons are now available to meet specific requirements of customers such as Reporting and Documentation, Safety and Traceability of Critical systems Designs, Management of obsolescence.

"We listened to our customers and we realized that these new features were needed to cement their numerous third-party tools from CAD vendors. The IP XACT-ready Magillem 4.1 platform is originally designed for our customers to preserve their independence from EDA vendors. These new features represent a major step forward to further enhance their design process within a context of multi location/multi competency teams and fragmented tasks development" says Cyril Spasevski, Magillem Chief Technology Officer.

Key customers applications: design specifications, virtual prototyping, verification, validation, legacy integration, etc. benefit from a common backbone and seamless encapsulation into one unified environment.

MAGILLEM undisputed advantage lies in the extended potential given to users in programming their flow control, effectively integrating various interoperable point tools and developing additional modules for missing functionalities.

Flexibility and versatility are provided by a powerful "Custom Generators Factory".

.../...

About MAGILLEM

Magillem Design Services provides customers with tools and services that drastically reduce the global cost of complex design, help them preserve their independence from EDA vendors and their investment by relying on a worldwide adopted format: IP-XACT by the SPIRIT consortium™ (soon to be IEEE 1685). Clients are semiconductors manufacturers (ASICs, ASSPs), system integrators and information technology companies engaged in the research, design, development, manufacturing and integration of advanced technology systems and products (using ASICs and FPGA). Magillem is headquartered in Paris, France with a subsidiary in the USA and sales offices in Asia. The Magillem team has been a contributor to the IP-XACT specification since 2003 and is the only vendor providing a tool implementing all the versions including the latest one of the specification defined by The SPIRIT Consortium.

Magillem has established partnerships with EDA vendors, alliances with ASICs leaders, universities, and R&D European. For more information, visit www.magillem.com

