



## MIP

### Magillem IP-XACT Packager

#### Overview

Magillem IP-XACT Packager is a scalable and fully automated solution that captures your legacy IP libraries from a wide range of formats, automatically creates an IP-XACT certified description, and enables you to import automatically legacy component libraries, thus facilitating the integration of your IP with any client directory structure.

#### Main features

- Targets any IP-XACT version, whether legacy (1.0 to 1.4), or IEEE1685 standard
- Guarantees the validity of generated files against IP-XACT grammar, semantics, and the Spirit/Accellera Consortium guidelines
- Captures IP from many different formats, and supports any kind of IP description, client data structure and input information thanks to a highly configurable process
- Connects to various client infrastructures, and provides direct links to major revision control systems (CVS, SVN, Clearcase, DesignSync)
- Features a scalable architecture which makes it possible to recapture a library of components following input changes
- Supports full or incremental packaging

Additional services proposed by Magillem include auditing existing libraries, identifying the most efficient import strategy, and customizing tools to handle customer-specific information and integrate with the client infrastructure.

#### Benefits

- For the IP provider : defines an IP in a reusable format that facilitates the IP configuration, implementation and verification. Provides a correct by construction IP-XACT description without any prerequisite IP-XACT expertise.
- For the IP integrator : supports various architectures and leverages the management of IP libraries.
- Facilitates project management tasks and collaborative work thanks to powerful verification, tracking and reporting tools.

#### Related resources

- Datasheet [Magillem IP-XACT Packager \(MIP\)](#)
- Packaging [Magillem Registers Engine \(MRE\)](#)

- Traceability : [Magillem Link Tracer \(MLT\)](#)
- Business case [Legal publications](#)
- [Virtual prototype Design solution](#)
- [RTL Design solution](#)
- [Analog Design solution](#)
- [Digital Design solution](#)
- [Hybrid Analog-Mix-Signal \(AMS\) Design solution](#)
- [All in a box Design solution](#)
- SafeCer Project
- Book : [Electronic Design Automation for IC System Design, Verification, and Testing](#)