Magillem Platform Assembly

Overview

« One platform, no hassle » : Magillem Platform Assembly (MPA) is a powerful and intuitive Integrated Design Environment to guide designers when configuring and assembling their platforms. MPA also helps streamline the exploration and the implementation of IP-based systems.

Magillem Platform Assembly can be used either as a stand-alone solution for assembling and interconnecting IPs, or in combination with other Magillem products like MIP to build an IP-XACT library, MGS to build and debug flow scripts, as well as MRV and MSE to manage the software interface.

Main features

- A complete graphic platform editor, which enables designers to build a complete view of their system with all the interconnections. In addition, MPA includes advanced parameterization tools, a reusable methodology, and collaborative features.
- A digital and/or analog design environment
- A set of predefined automated operations to easily connect IP through the hierarchy, manipulate components and reduce error risks
- Support for a virtual hierarchy to describe the platform hierarchically, according to the flow step
- A comprehensive verification toolkit including a large range of connectors to EDA tools, RTL and ESL Netlisters, a facility to drag and drop IP into design, and much more
- An IP integrator to import and update complex IP bundles and a dashboard environment to control and monitor the design under test

Benefits

- For hardware and software architects : provides an efficient environment to streamline architecture exploration and definition.
- For IP providers : packages IP in a reusable format to guide IP integrators through all their design steps.
- For IP integrators : supports heterogeneous architecture integration, generates automatically ESL/HDL code and facilitates the management of IP libraries.
For project managers: facilitates project management tasks and collaborative work thanks to powerful verification, tracking and reporting tools.

For verification engineers: provides efficient monitoring, configuration, initialization, and validation tools, as well as a coverage scoring facility.

Related resources

- Datasheet Magillem Platform Assembly (MPA)
- Business case Semiconductors
- Requirement based UVM Verification and Traceability
- Paper DVCon Europe 2014 RL
- White paper DAC 2015: Extending IP-XACT and UPF to support ESL to RTL low power Design methodology Ev et GA
- Projet DGE-Socket 2008-2012
- Virtual prototype Design solution
- RTL Design solution
- Analog Design solution
- Digital Design solution
- Hybrid Analog-Mix-Signal (AMS) Design solution
- All in a box Design solution
- SafeCer Project