



Magillem Software Exec

Overview

Magillem Software Exec (MSX) includes a unique set of features to automate the creation and the configuration of Virtual Prototyping platforms from a library of open source SystemC models of Processors, Memories, Interconnects and Peripherals packaged in IP-XACT. Based on the open source SystemC standard simulator, Magillem Software Exec provides a simple and straightforward solution to execute, debug, and analyze your cross-compiled software on the target processor of your choice.

Main features

- Based on the open IEEE SystemC TLM and IP-XACT standards
- Includes a direct path to the Accellera Open SystemC simulator
- Integrates best-in-class technologies developed by two Magillem partners : processor simulation models from Imperas and TLM technology from ST Microelectronics.

Benefits

- Reusable and vendor-neutral flow thanks to native integration of IP-XACT standard
- Enables software development and software-driven validation teams to quickly validate their device drivers or baremetal software on a high-speed functional simulation model of their digital IP or System.
- The Imperas technology available in MXS includes processor high-speed functional simulation models and the Imperas Developer tools to execute and debug embedded software code on the processor models.
- The ST Microelectronics TLM technology packaged in MXS includes SystemC libraries, SystemC code generator for IP, SystemC Makefile generator, as well as generic configurable models.

Related resources

- Datasheet Magillem Software Exec (MSX) coming soon
- Embedded Software : [Magillem Sequence Editor \(MSE\)](#)



Sustainable innovation to drive core business efficiency

- [Virtual prototype Design solution](#)
- [RTL Design solution](#)
- [Digital Design solution](#)
- [Hybrid Analog-Mix-Signal \(AMS\) Design solution](#)
- [All in a box Design solution](#)